

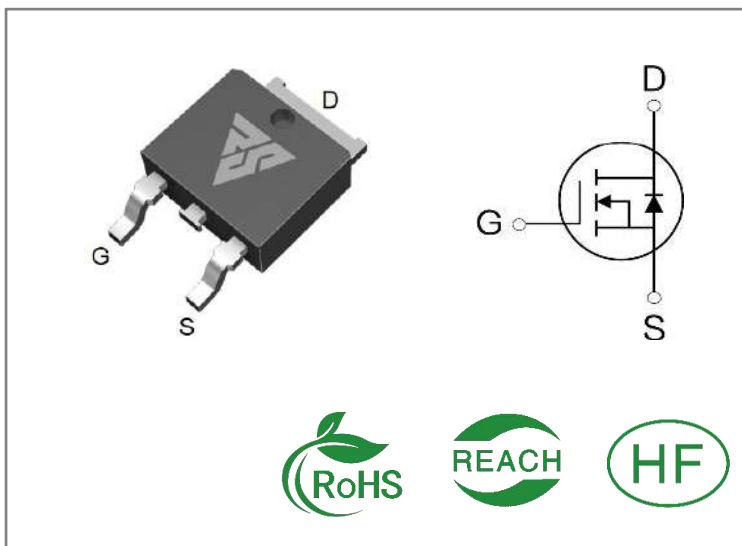
ID	R _{DS(ON)} (Typ)	VDSS
6A	1.2Ω	500V

Applications:

- Switch Mode Power Supply(SMPS)
- Adapter & Charger
- AC-DC Switching Power Supply

Features:

- Fast switching speed
- 100% avalanche tested
- Improved dv/dt capability



Ordering Information

Part Number	Package	Marking	Packing	Qty.
RS6N50D	T0-252	RS6N50D	Tape&reel	2500 PCS

Absolute Maximum Ratings Tc= 25°C unless otherwise specified

Symbol	Parameter	RS6N50D	Units
VDSS	Drain-to-Source Voltage	500	V
ID	Continuous Drain Current TC=25°C	6	A
IDM	Pulsed Drain Current (Note*1)	24	
PD	Power Dissipation	78	W
VGS	Gate- to- Source Voltage	±30	V
EAS	Single Pulse Avalanche Energy L = 10mH, VDD = 50V, RG = 25 Ω	88	mJ
TL TPKG	Maximum Temperature for Soldering	300 260	°C
	Leads at 0.063in(1.6mm)from Case for 10 seconds		
	Package Body for 10 seconds		
TJ and TSTG	Operating Junction and Storage Temperature Range	-55 to 150	

* Drain Current Limited by Maximum Junction Temperature

Caution: Stresses greater than those listed in the“ Absolute Maximum Ratings” Table may cause permanent damage to the device.

Thermal Resistance

Symbol	Parameter	RS6N50D	Units	Test Conditions
R θ JC	Junction-to-Case	1.58	°C / W	Drain lead soldered to water cooled heatsink, PD adjusted for a peak junction temperature of + 1 5 0 °C
R θ JA	Junction-to-Ambient	62.5		1 cubic foot chamber, free air.

OFF Characteristics T_J= 25°C unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
BVDSS	Drain- to- source Breakdown Voltage	500	--	--	V	V _{GS} =0V, I _D =250μA
IDSS	Drain- to- Source Leakage Current	--	--	1	μA	V _D S=500V, V _{GS} =0V
IGSS	Gate- to- Source Forward Leakage	--	--	100	nA	V _{GS} =30V , V _D S=0V
	Gate- to- Source Reverse Leakage	--	--	-100		V _{GS} =-30V , V _D S=0V

ON Characteristics T_J=25°C unless otherwise specified

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
R _{DS(on)}	Static Drain- to- Source On-Resistance(Note*2)	--	1.2	1.5	Ω	V _{GS} =10V, I _D =3A
V _{GS(TH)}	Gate Threshold Voltage	3	--	4	V	V _{GS} =V _D S, I _D =250μA

Resistive Switching Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
t _{d(ON)}	Turn- on Delay Time	--	7	--	nS	V _D S=250V I _D =6A R _G =25Ω
t _{rise}	Rise Time	--	105	--		
t _{d(OFF)}	Turn- OFF Delay Time	--	16	--		
t _{fall}	Fall Time	--	7	--		

Dynamic Characteristics Essentially independent of operating temperature

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
Ciss	Input Capacitance	--	535	--	pF	VGS=0V VDS=25V f=1.0MHz
Coss	Output Capacitance	--	63	--		
Crss	Reverse Transfer Capacitance	--	4.7	--		
Qg	Total Gate Charge	--	14.4	--	nC	VDS=400V ID=6A VGS=10V
Qgs	Gate- to- Source Charge	--	2.8	--		
Qgd	Gate-to-Drain(" Miller") Charge	--	6.8	--		

Source- Drain Diode Characteristics

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
IS	Continuous Source Current	--	--	6	A	Integral pn- diode in MOSFET
ISM	Maximum Pulsed Current	--	--	24	A	
VSD	Diode Forward Voltage	--	--	1.4	V	IS=3A,VGS=0V
trr	Reverse Recovery Time	--	400	--	nS	VGS=0V IS=6A,di/dt=100A /μs
Qrr	Reverse Recovery Charge	--	3.5	--	μC	

Notes:

- * 1. Repetitive rating, pulse width limited by maximum junction temperature.
- * 2. Pulse Test: Pulse width ≤ 300μs, Duty Cycle ≤ 1%

Typical Feature Curve

Figure 1. Output Characteristics ($T_J = 25^\circ\text{C}$)

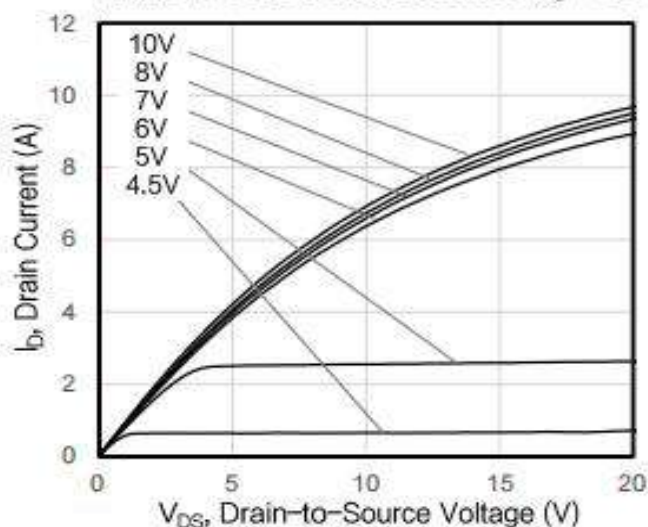


Figure 2. Body Diode Forward Voltage

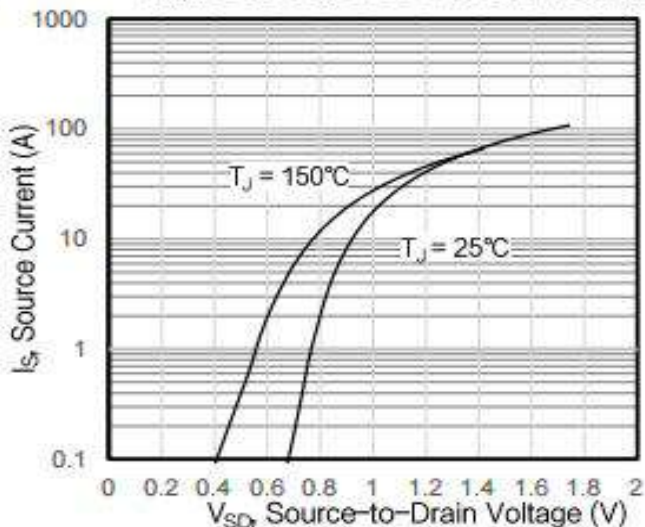


Figure 3. Drain Current vs. Temperature

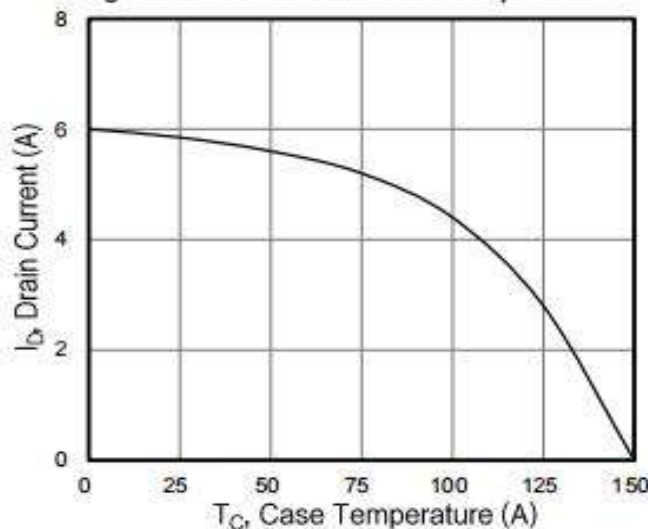


Figure 4. Power Dissipation vs. Temperature

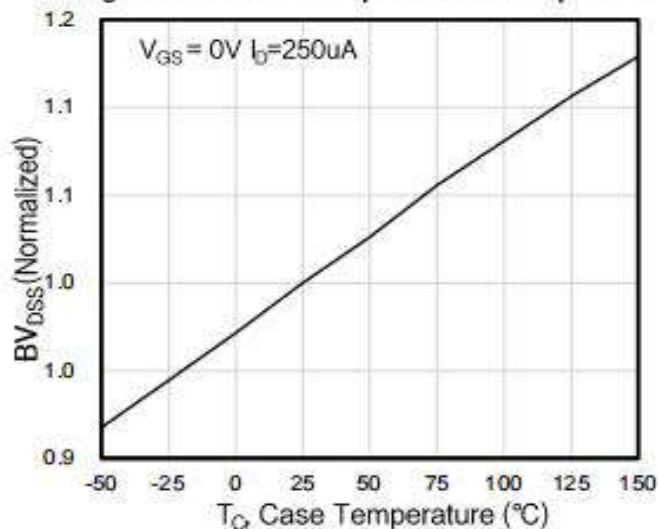


Figure 5. Transfer Characteristics

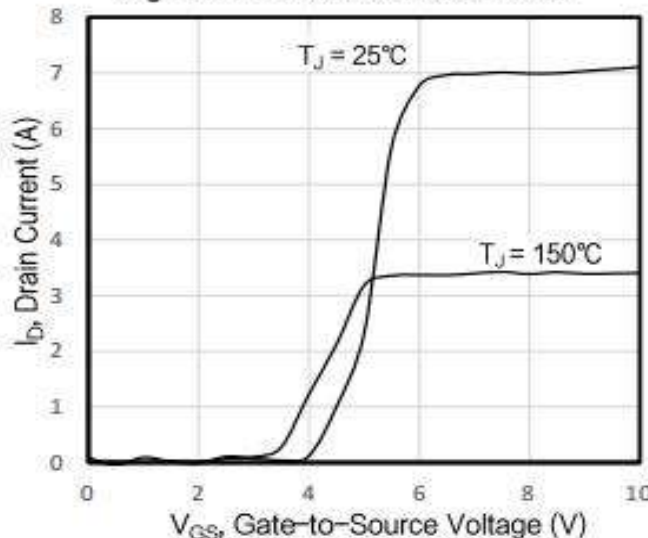


Figure 6. On-Resistance vs. Temperature

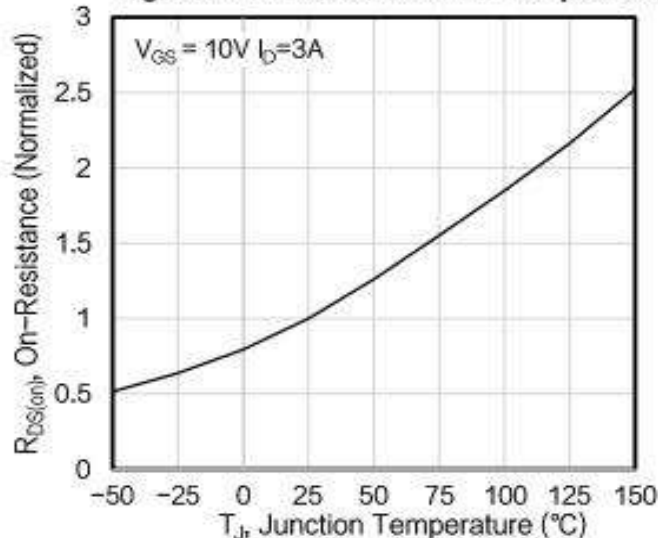


Figure 7. Capacitance

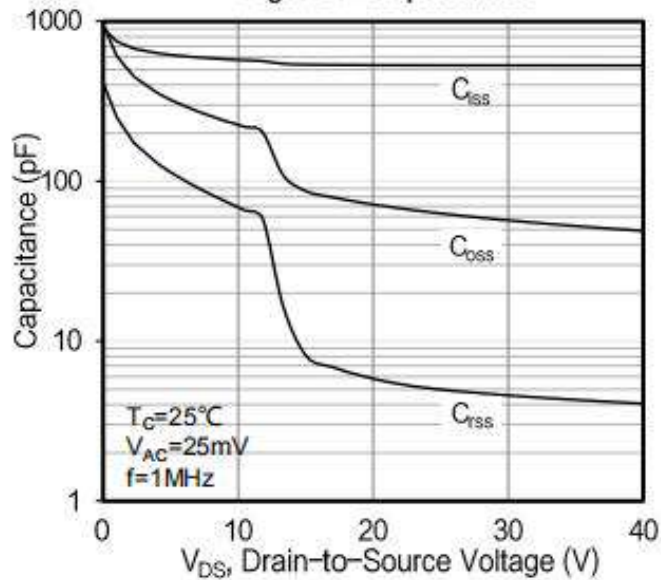


Figure 8. Gate Charge

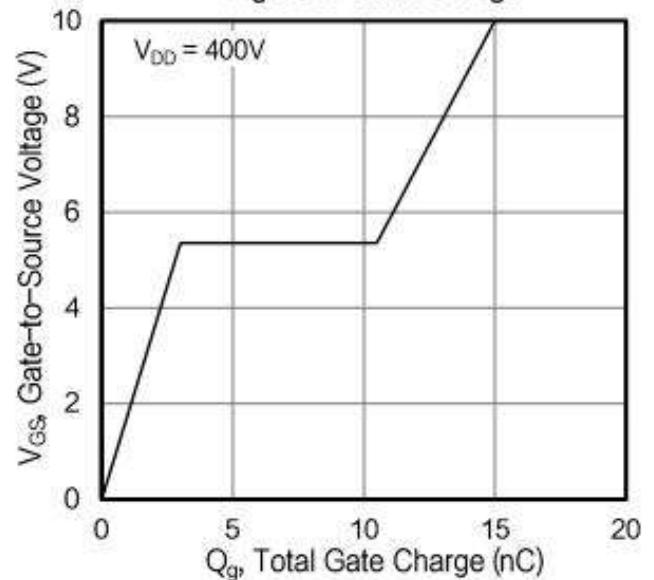


Figure 9. Transient Thermal Impedance

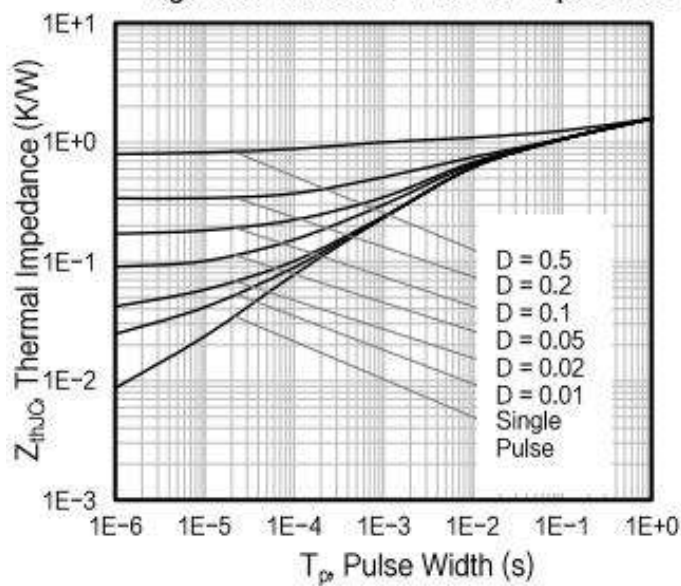
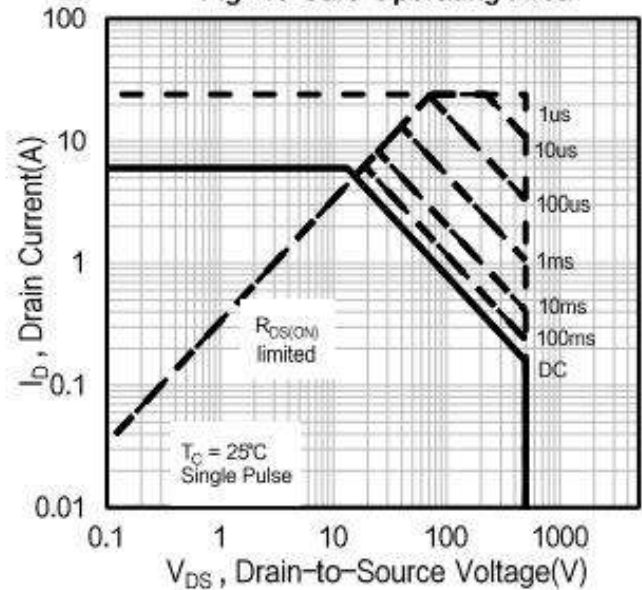


Fig. 10 Safe Operating Area



Test Circuits and Waveforms

Figure A: Gate Charge Test Circuit and Waveform

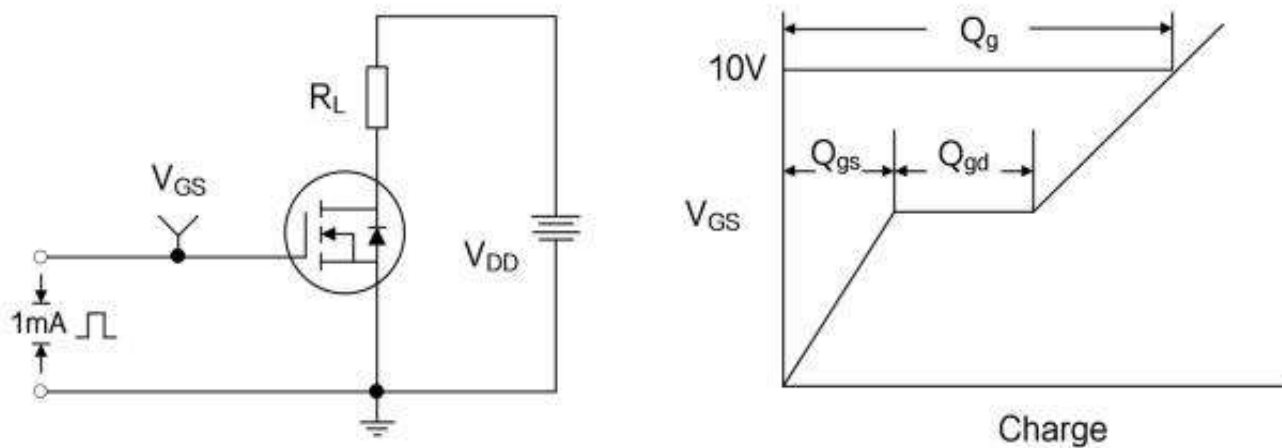


Figure B: Resistive Switching Test Circuit and Waveform

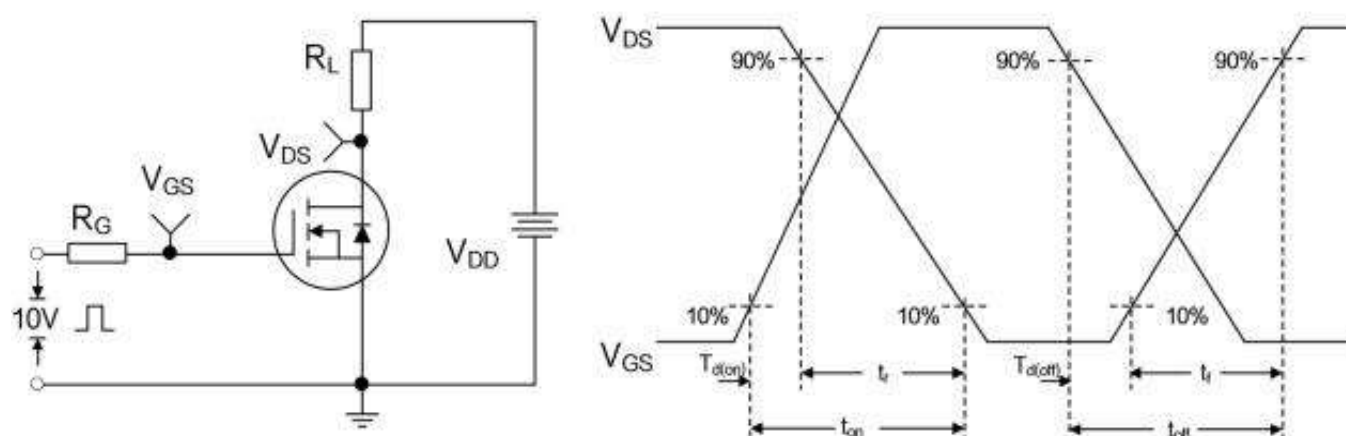
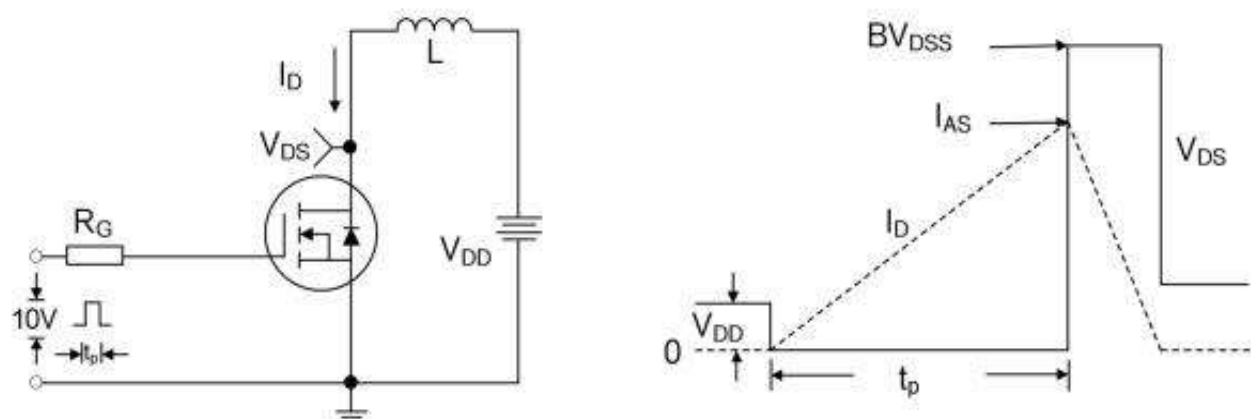
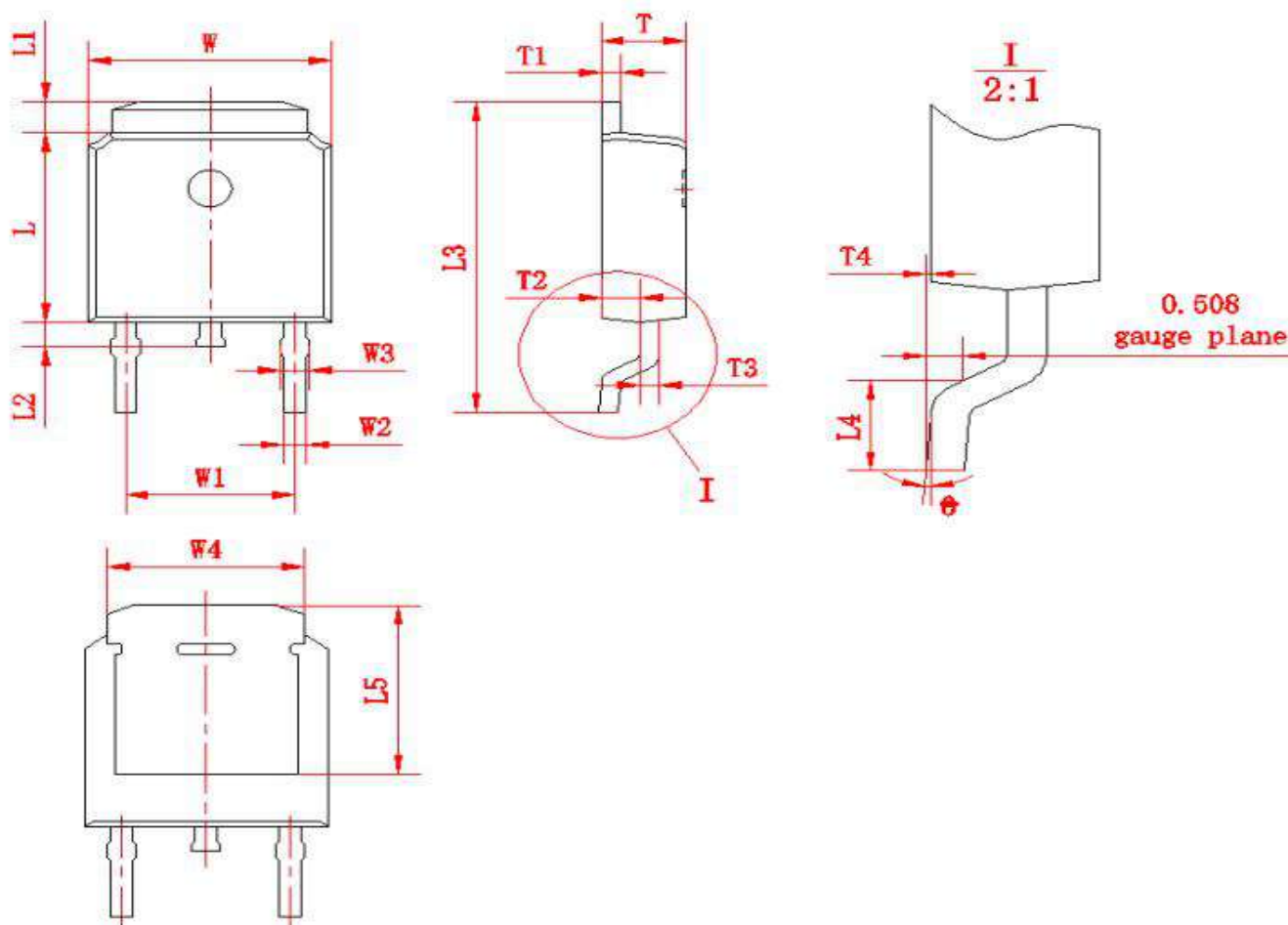


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



Package outline drawing(TO-252 Unit: mm)



符号	尺寸		符号	尺寸		符号	尺寸	
	Min	Max		Min	Max		Min	Max
W	6.50	6.70	L1	0.80	1.20	T1	0.48	0.58
W1	(4.572)		L2	0.60	1.00	T2	0.95	1.15
W2	0.6	0.8	L3	9.70	10.30	T3	0.48	0.58
W3	0.68	0.88	L4	1.30	1.70	T4	0.00	0.12
W4	(5.3)		L5	(5.20)		0	0	8
L	6.00	6.20	T	2.20	2.40			

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